

Notice of References Cited

Application/Control

09/801,241

Applicant(s)/Patent Under
Reexamination
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,859,975	01-1999	Brewer et al.	709/213
	B	US-6,628,662	09-2003	Blackmon et al.	370/447
	C	US-6,125,429	09-2000	Goodwin et al.	711/143
	D	US-5,559,970	09-1996	Sharma, Vinod	710/317
	E	US-6,070,003	05-2000	Gove et al.	710/317
	F	US-5,878,240	03-1999	Tomko, Lawrence Andrew	710/316
	G	US-5,696,913	12-1997	Gove et al.	710/317
	H	US-5,471,592	11-1995	Gove et al.	709/213
	I	US-5,226,125	07-1993	Balmer et al.	710/317
	J	US-5,081,575	01-1992	Hiller et al.	710/317
	K	US-6,519,672	02-2003	Scardamalia et al.	710/317
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP 08339326 A	12-1996	Japan	KISE et al.	G06F 12/02
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Opsommer, J., et al., "A VLSI Processor-Switch for a Dual IEEE-796 Bus with Shared and Dual-Port Memories," May 8-12, 1989, IEEE, CompEuro '89, VLSI and Microelectronic Applications in Intelligent Peripherals, Proceedings, p. 4/146-4/149.
	V	Aude, J.S., et al., "A High-Performance Switching Element for a Multistage Interconnection Network," Sept. 30 - Oct. 3, 1998, IEEE, XI Brazilian Symposium on Integrated Circuit Design, 1998. Proceedings, p. 154-157.
	W	Ramesh, T., et al., "Bus Arbitration in an Embedded Processor-Shared Multiprocessor System," Aug. 16-18, 1993, IEEE, Proceedings of the 36th Midwest Symposium on Circuits and Systems, Vol. 1, p. 320-322.
	X	Bos, M.L., "Design of a Chip Set for a Parallel Computer Based on the Crossbar Interconnection Principle," Aug. 13-16, 1995, IEEE, Proceedings of the 38th Midwest Symposium on Circuits and Systems, Vol. 2, p. 752-756.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.